



Anti-Phase-Boundary Defects in GaAs-on-Si Films: 1. characterization by SHG 2. suppression by ART

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The microelectronics industry is trying to marry III-V and Column IV semiconductors via hetero-epitaxy to combine the favorable properties of each





Hsu, Nanotech. 23, 495306 (2012)

GaAs/Si interfaces are susceptible to formation of defects



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Anti-Phase Domains (APDs) **Threading Dislocations (TDs)** Where: polar-on-nonpolar hetero-interfaces Where: any hetero-interface Cause: single-atom steps, nonpolar substrate (1) Cause: lattice mismatch Kawabe, JJAP 26, L944 (1987) TDs • As misfit 🗆 Ga dislocation • Si misfit dislocation [001] (111)<100> substrate [010] [100] Characterization: selective Ga-Ga bonds along Anti-Phase (011) etching + TEM **Boundaries** (APBs) degrade carrier mobility typical lattice GaAs/Si(001) sub-TDD mismatch strate ΓFN w. GaAs [cm⁻²] TDs and APBs are chall-Si 4% >10⁹ enging to distinguish in (001)**TEM micrographs** Ge um – <108 <1% (001)

Brammertz, TSF 517, 148 (2008)

To evaluate strategies for suppressing these defects, a fast, noninvasive diagnostic that clearly distinguishes APBs from TDs is needed



Neighboring APDs generate SH fields of opposite sign







SHG characterizes APBs sensitively and non-invasively



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To test SHG sensitivity to TD Density (TDD), we prepared In_xGa_{1-x}As/GaAs samples



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Conclusion: SHG is uncorrelated with TDD



Substrate off-cut angle α strongly affects APD density & SHG suppression







Scanning SHG microscope yields mottled SHG response from APD-laden surfaces

Lei et al., APL 102, 152103 (2013)



sample rastered in focal plane

- The SHG images are NOT direct maps, but rather higher-order moments, of the APD distribution.
- **Bright areas** indicate dominance of one type of domain within the laser spot.
- **Dark areas** indicate equal areas of $+\chi^{(2)}$ and $-\chi^{(2)}$ domains within the laser spot.
- SHG NSOM* may be able to image individual APDs directly.





*Smolyaninov, *Phys. Rev. B* **56**, 9290 (1997) Bozhevolnyi, *Opt. Commun.* **150**, 49 (1998)



Growth of GaAs on exactly oriented Si(001) is preferred for high-volume manufacturing



Aspect-Ratio Trapping (ART) is an established technique for suppressing TDs on Si(001) Fitzgerald, J. Electron. Mater. 20, 839 (1991) GaAs [110] SiO₂ **TD**-free trench region trapped walls TDs All TDs trapped for $b/a > \sqrt{2}$ Si(001) 100 nm 3D geometry: [110] trench sidewall screw threading 771 segmen



We found (serendipitously) that ART patterning of oriented Si(001) substrates also dramatically suppresses APDs in GaAs epi-films



Nearly complete recovery of GaAs reference SHG signal!



GaAs pillars evidently coalesce commensurately into a single domain epi-layer





tapered sidewall with pliable Si-O bonds may encourage Ga-As bond formation when pillars merge

ART appears to solve 2 problems simultaneously!



SUMMARY



- SHG characterizes APDs in polar-on-nonpolar semiconductor epifilms sensitively, quickly, non-invasively and selectively.
- Scanning SHG microscopy indirectly probes APD size distribution; SHG-NSOM promises direct APD imaging.
- SHG APD probe helps develop methods to suppress APDs: *e.g.* 1. vicinal substrates; 2. ART
- Compared to RAS, SHG is equally useful as an *ex-situ* & *in-situ* APB probe, requires only a single-λ source for any material system, and enables microscopic (possibly single APD) imaging.

Lei et al., Appl. Phys. Lett. 102, 152103 (2013) Patent Pending



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